

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q79663

Junichi YAMADA

Appln. No.: 10/773,290

Group Art Unit: 2818

Confirmation No.: 8732

Examiner: Ly D. PHAM

Filed: February 09, 2004

For:

SEMICONDUCTOR MEMORY HAVING A DEFECTIVE MEMORY CELL

RELIEVING CIRCUIT

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated May 27, 2004 in which the Examiner has required a restriction, Applicant responds by electing Group I, i.e., claims 12-15. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,

Registration No. 25,665

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Date: June 28, 2004